



Attorney Docket No. 0553-0166.01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Shunpei YAMAZAKI et al

Serial No.: 10/629,069

Filed: July 29, 2003

Art Unit: 2826

Examiner: Fetsum Abraham

For: WIRING MATERIAL AND A SEMICONDUCTOR)
DEVICE HAVING A WIRING USING THE
MATERIAL, AND THE MANUFACTURING METHOD THEREOF

I hereby certify that this correspondence is
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Shanna Wallace

Date: June 14 2006

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. Copies of the references are enclosed herewith for the Examiner's consideration.

REFERENCES CITED HEREIN

The references cited herein were first disclosed by Applicants in an IDS filed on July 29, 2003. Each of these references was either previously submitted or was cited by the Patent Office in parent application 09/527,437, which is relied

upon for an earlier filing date under 35 USC 120. Therefore, under 37 CFR §1.98(d), it is not necessary to file these references. However, to ensure that these references are considered, Applicants are now submitting a copy of each foreign reference and publication for the Examiner's entry and consideration.

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

FEE

Applicants do not believe a fee is due for this IDS as this
IDS and references were already properly submitted and cited.
However, if any fee is required for this IDS, please charge our
Deposit Account No. 50/1039.

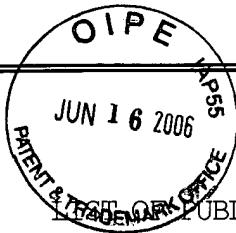
Respectfully submitted,



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PUBLICATIONS CITED BY APPLICANT	<u>Atty. Docket No.</u> 0553-0166.01	<u>Serial No.</u> 10/629,069
	<u>Applicant</u> Shunpei YAMAZAKI et al	
	<u>Filing Date</u> July 29, 2003	<u>Group</u> 2826

US PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	English Abstract	English Trans.	FILING DATE
	JP 05-093267	04/16/93	Hitachi Metals Ltd.	X		10/01/91
	JP 05-263226	10/12/93	Fujitsu Ltd.	X		03/17/92
	JP 07-076771	03/20/95	Japan Energy Corp.	X		09/08/93
	JP 07-130652	05/19/95	Semiconductor Energy Lab Co. Ltd.	X		10/29/93
	JP 08-153722	06/11/96	Toshiba Corp.	X		09/29/95
	WO 95/16797	06/22/96	Toshiba KK	X		12/14/94
	EP 0 735 152 A1	10/02/96	Kabushiki Kaisha Toshiba			12/14/94
	JP 10-247735	09/14/98	Semiconductor Energy Lab Co. Ltd.	X		03/03/97
	JP 11-133463	05/21/99	Semiconductor Energy Lab Co. Ltd.	X		10/31/97
	EP 0 947 593 A2	10/06/99	Kabushiki Kaisha Toshiba			12/14/94
	JP 2001-056485	02/27/01	Semiconductor Energy Lab Co. Ltd.	X		07/22/99

**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

(Include name of author (in CAPITAL LETTERS), title of article or item (book, magazine, journal, serial, symposium, catalog, etc.) date, pages(s), volume-issue number(s), publisher, city and/or country where published).

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| | <ol style="list-style-type: none">1) YOSHIDA, T. et al, "A Full Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, (1997).2) FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCD Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, pp. 782-785, (1998). |
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EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.